

Appl. No. 10/523,891; Docker No. NL02 0720US
Amdt. dated February 8, 2006
Reply to Office Action of January 11, 2006

Amendments to the Claims

1. *(Cancelled)*

2. *(Currently Amended)* ~~A voltage-controlled oscillator as claimed in claim 1, A voltage-controlled oscillator comprising a LC tank circuit coupled to a pair of transistors and crossed-coupled to a pair of emitter follower transistors, each transistor having a collector, an emitter and a base, the voltage controlled oscillator being characterized in that a supply voltage applied to the collectors of the emitter follower transistors is substantially different from a supply voltage applied to the bases of the emitter follower transistors, and~~

wherein the LC tank circuit is coupled to the supply voltage via a bipolar transistor connected as a diode for obtaining a substantially different supply voltages for the bases and collectors of the emitter follower transistors.

3. *(Previously Presented)* A pseudo random sequence generator comprising a first sequence generator and a second sequence generator driven by a voltage controlled oscillator as claimed in Claim 1, a first output of the first sequence generator and a second output of the second sequence generator being coupled to a multiplexer driven by an output signal of the voltage controlled oscillator for selecting either a signal outputted by the first sequence generator or a signal outputted by the second sequence generator, the multiplexer generating at a third output a binary signal having a bit-rate that is substantially double a bit-rate obtained either at the first output or at the second output.

4. *(Previously Presented)* A pseudo random sequence generator as claimed in Claim 3, wherein each of the sequence generators comprises a closed-chain of flip-flops each having a data input, a clock input, a preset input and an output, the pseudo random sequence generator further comprising a feedback including a XOR gate having an output coupled to a first of the flip-flops data input and a pair of inputs coupled to a pair of outputs of the flip-flops.